

☐ UNCLASSIFIED ☒ CONFIDENTIAL ☐ SECRET (specify)  
Approved For Release 2003/12/04 : CIA-RDP78B05171A000100010087-3

# REQUEST TO PROCUREMENT DIVISION FOR SERVICES

(other than property or building maintenance and repairs)

OFFICE/DIV/BR NPIC/TSSG/SSD/LB	REQUEST NO. 55-6228-60	DATE OF REQUEST 22 Jan. 1969	I CERTIFY THAT FUNDS IN THE ESTIMATED AMOUNT OF \$ <u>53,460</u> AVAILABLE. CHARGE TO FAN <u>9155-5340</u>
PROJECT TITLE Stereo Comparator	PROJECT OR CONTACT OFFICER [REDACTED]	EXTENSION	SIGNATURE OF BUDGET OFFICER [REDACTED] DATE
PROPOSAL NO. AND DATE			CONTRACT & TASK ORDER NO. (if known)

## TYPE OF SERVICE REQUESTED

☒ RESEARCH/DEV  
☐ GRANT  
☐ REPAIR  
☐ MAINTENANCE  
☐ MODIFICATION

TRANSFER OF FUNDS TO  
OTHER GOVT. AGENCY  
(specify if applicable)

## APPLICABLE ONLY TO REPAIRS AND MODIFICATIONS

TECHNICAL INSPECTION IS REQUIRED BY

☐ RECEIVING DEPOT T & I

☐ TECHNICAL MONITOR

☐ ITEMS TO BE PICKED UP OR ☐ SERVICES PERFORMED AT:

ITEMS TO BE REDELIVERED TO:

PRIORITY SC 0 <input checked="" type="checkbox"/> SC 1 SC 2	CONTRACT CLASSIFICATION <b>CONFIDENTIAL</b>	WORK CLASSIFICATION	HARDWARE CLASSIFICATION	REPORTS CLASSIFICATION
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SHORT SUBSTANTIVE TITLE AND/OR DESCRIPTION OF SERVICE TO BE PERFORMED

Increase funding by [REDACTED] cover change of scope  
on the Stereo Comparator Contract [REDACTED]

APPROVED: D/NPIC, 17 Jan 1969

Declass Review by  
NIMA/DOD

(See reverse for specific information  
required on R&D requests.)

## APPROVAL

SIGNATURE

DATE

DESIGNATION

C/LB/SSD/TSSG/NPIC

## PROCUREMENT DIVISION USE

DATE RECEIVED IN PD

RECORDED BY

SECTION ASSIGNED TO

NEGOTIATOR

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FORM 2420  
3-67

REPLACES FORMS  
2420 AND 2416  
WHICH MAY BE

☐ UNCLASSIFIED

☒ CONFIDENTIAL

☐ SECRET

(specify)  
(5-12-98)

☐ UNCLASSIFIED ☐ CONFIDENTIAL ☐ ATTACHMENTS OR STATEMENTS ☐ SECRET ☐ \_\_\_\_\_ (specify)

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1 JUSTIFICATION FOR SOURCE SELECTION

2. PROPOSAL

3. ☒ VERABLE ITEMS

REPORTS REQUIRED

\_\_\_\_ NO. OF COPIES

☐ MONTHLY

☐ INTERIM

☐ QUARTERLY

☐ FINAL

HARDWARE (*state type and number*)

OTHER

4. GFE REQUIRED

5. SPECIAL INSTRUCTIONS

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☐ UNCLASSIFIED ☐ CONFIDENTIAL ☐ SECRET ☐ \_\_\_\_\_ (specify)

8 October 1968

U. S. Government

Subject: Quotation for High Speed Tape Reader/Punch with  
Interface Chassis

Gentlemen:

25X1 [redacted] is pleased to submit their offer  
25X1 to provide one High Speed Tape Reader/Punch with a special interface chassis  
at a price of [redacted]

This price includes delivery and installation, and is firm for a  
period of ninety (90) days.

25X1 [redacted] The Stereocomparator as presently designed includes an ASR 35  
Teletype unit with paper tape punch and reader. This combination processes  
information at the rate of ten (10) characters per second, which while adequate  
for system information, would not be satisfactory for program read in or dump,  
or for major program update schedules.

For example, the time required to load the FORTRAN systems and  
the Input/Output library object tapes would be in the range of 32 to 45 minutes.  
In addition, the time spent in read in and dump during testing of a new or updated  
program results in hours, or even weeks, of additional programmer personnel time.

The provision of a suitable high speed Reader/Punch is thus  
virtually essential when programmer efficiency is considered.

25X1 A further advantage to this proposal is that the added equipment  
makes the [redacted] DDP 516 computer available as a functioning separate,  
general purpose computer with a 16K memory core.

The device consists of two separate units with the necessary  
interconnecting and input/output cables.

U. S. Government  
10/8/68 - Continued

Page 2.

25X1 The tape unit consists of a [ ] photoelectric reader with an [ ] Teletype punch in a [ ] table top enclosure. The reading speed is 300 characters per second, and the punch speed is 110 characters per second. A standard 8-level code is read or punched in one inch wide lubricated paper or Mylar tape, with a minimum opacity of 40%.

The Interface unit consists of a solid state circuitry in a chassis arranged for mounting in the rear of a standard 19-inch rack.

The chassis circuitry includes the necessary power supplies to operate the logic components.

The logic assemblies include the following functions:

1. Isolation of the Reader/Punch from the computer output, input, address and control lines to maintain the critical impedance and loading characteristics of the computer system.
2. Perform address decoding to enable the computer to issue commands and recognize data from the Reader/Punch.
3. Supply synchronization and timing signals as necessary to make the high clock rate of the computer compatible with the Reader/Punch strobe rates.
4. Provide necessary line drivers and receivers with cable so that the Reader/Punch can be moved about in the work area as conveniently as possible.
5. Shift voltage levels to make the Reader/Punch logic compatible with the computer.

Connectors and cables are provided to interconnect the computer, the rack cabinets, the Reader/Punch and the interface chassis.

If additional information is required regarding the above quotation, please do not hesitate to contact us.

Very truly yours, ,

[Signature]

Administration Manager

BCJ:jm